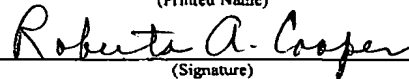




Atty. Dkt. No. 039153-0693 (H1718)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Xiang et al.
Title: SHALLOW TRENCH
ISOLATION PROCESS
AND STRUCTURE WITH
MINIMIZED STRAINED
SILICON CONSUMPTION
Appl. No.: 10/755,602
Filing Date: 01/12/2004
Examiner: Jack Chen
Art Unit: 2813
ConfirmationNo.: 9549

CERTIFICATE OF EXPRESS MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
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Roberta A. Cooper (Printed Name)	
 (Signature)	

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

ATTENTION: DRAWING REVIEW BRANCH

Sir:

Transmitted herewith are the formal drawings (8 sheets, Figures 1-15) for the above-identified application. The Official Draftsperson is respectfully requested to approve these drawings for entry into the application.

Respectfully submitted,

Date 10/20/2005

By 

FOLEY & LARDNER LLP
Customer Number: 34083
Telephone: (313) 234-7150
Facsimile: (313) 234-2800

Marcus W. Sprow
Attorney for Applicant
Registration No. 48,580